

0.3 Megapixel camera module with built-in ISP

NCM03-DSA is a color module with built-in ISP that complies with VGA pixels. It has 640 horizontal and 480 vertical pixels and via 1/4" optical format. Features included low power consumption, on-chip primary colors color filter, and small size by adjusting signal processing.

1. Features

- Small all-in package
- Equipped with a compact wide-angle lens
- I2C
- Low Power consumption

2.Specification

- Image Sensor
- Shutter Type
- Dimension
- Effective Pixels
- Pixel Size
- Output Interface
- Output Signal Format
- Frame rate (Max.)
- Function

Rolling shutter $13 \times 21 \text{ [mm] } \text{\%1}$ $640(H) \times 480(V)$ $5.6\,\mu\,{
m m(H)}\, imes\,5.6\,\mu\,{
m m(V)}$ Digital 8bit parallel YUV422, RGB565, RGB444 30fps Auto Exposure Control, Auto White Balance, Auto Gain Control, Image Inversion (up/down, left/right), Flicker correction, etc. (H)102°/(V)76°/(D)134° IRCF F2.8 FPC 130 [mW]

1/4-inch CMOS color sensor

- Angle of View (Typ
- Optical filter
- F Number
- Connection Type
- Power Consumption (Typ.) %1 : FPC/FFC part is not included

3. Recommended Operating Conditions

Analog Power Supply	3.3 ± 0.33	[V]
• I/O Supply (HVDD)	3.3 ± 0.33	$[V] *_2$
• Operating Temperature	-20 \sim +60	[°C] **3
• Storage Temperature	$-30 \sim +70$	[°C] **3
2 : I/O and digital power supplies are common.		
※3 : No condensation		

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NCM03-DSA

4. Terminal Description

							↓ 1pin
No	Symbol	I/O	Terminal Description	No.	Symbol	I/O	Terminal Description
1	N.C.	-	Not connected	12	DATA6	0	Digital data output (Data6)
2	DGND	-	Digital ground	13	DATA5	0	Digital data output (Data5)
3	PCLK	0	Data clock	14	DATA4	0	Digital data output (Data4)
4	DGND	•	Digital ground	15	DATA3	0	Digital data output (Data3)
5	HSYNC	0	Horizontal synchronous signal	16	DATA2	0	Digital data output (Data2)
6	VSYNC	0	Vertical synchronous signal	17	DATA1	0	Digital data output (Data1)
7	HVDD	-	I/O power supply (common for digital power supply)	18	DATA0	0	Digital data output (Data0)
8	SDA	I/O	I2C Serial data	19	STDBY	Ι	Standby mode
9	SCL	Ι	I2C Serial Clock	20	MCLK	Ι	System clock
10	RESET	Ι	System reset	21	AGND	-	Analog ground
11	DATA7	0	Digital data output (Data7)	22	AVDD	-	Analog power supply

0.3±0.05

4±0.5

5. External Dimensions

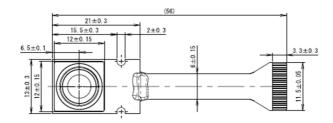
φ11±0.15 φ9±0.1

(10.5)

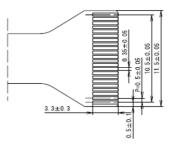
8±0.2

t. 3±0.2

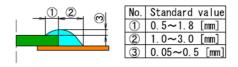
0.6±0.1



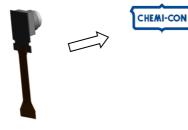
FPC terminal detail



Detail drawing of FPC reinforcement adhesion part



6. Image Capture Polarity





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Nippon Chemi-Con Corporation 2023.9.29

[Unit : mm]